



PATENT
Attorney Docket No. QCS-001DV3

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APR - 3 2003

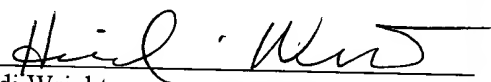
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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

APPLICANT(S): Kamieniecki et al.
SERIAL NO.: 09/932,754 GROUP NO.: 2829
FILING DATE: August 17, 2001 EXAMINER: J. Hollington
TITLE: Real-Time-In-Line Testing of Semiconductor Wafers

CERTIFICATE OF FIRST CLASS MAILING UNDER 37 C.F.R. 1.8

I hereby certify that this correspondence, and any document(s) referred to as enclosed herein, is/are being deposited with the United States Postal Service as first class mail, postage prepaid, in an envelope addressed to the Commissioner for Patents, Washington, DC 20231 on this 28th day of March, 2003.


Heidi Wright

Commissioner for Patents
Washington, D.C. 20231

Sir:

Submitted herewith is/are:

- (1) Transmittal Form (1 pg.);
- (2) Associate Power of Attorney (1 pg.); and
- (3) a Return Receipt Postcard



TRANSMITTAL FORM

Application Serial Number	09/932,754
Filing Date	August 17, 2001
First Named Inventor	Kamieniecki et al.
Group Art Unit	2829
Examiner Name	J. Hollington
Attorney Docket No.	QCS-001DV3
Patent No.	Not Yet Applicable
Issue Date	Not Yet Applicable

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ENCLOSURES (check all that apply)

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PATENT
Attorney Docket No. QCS-001DV3 (6695/2)

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

APPLICANT(S): Kamieniecki et al.
SERIAL NO.: 09/932,754 GROUP NO.: 2829
FILED: August 17, 2001 EXAMINER: J. Hollington
TITLE: Real-Time-In-Line Testing of Semiconductor Wafers

Commissioner for Patents
Washington, D.C. 20231

ASSOCIATE POWER OF ATTORNEY

Sir:

An associate power of attorney is hereby granted to:

NAME	REG. NO.
Stephanie M. Zierten	52,397

in connection with the above-identified patent application.

Please continue to direct all correspondence relating to the above application to:

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Respectfully submitted,

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Date: March ²⁶, 2003
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